

In the Specification:

Please amend the Specification as follows:

Please substitute the following for the paragraph beginning at page 2, line 3:

An output clock signal CLK_OUT is outputted from the master circuit (not shown) and sent to the slave circuit (not shown). An input clock signal CLK_IN is outputted from the slave circuit and sent to the master circuit. ~~The input clock signal CLK_IN is a feedback clock signal of the output clock signal CLK_OUT.~~

Please substitute the following for the paragraph beginning at page 4, line 3:

In the clock forwarding method, fixed initial parameters necessary for data loading and unloading must be determined. In general, the fixed initial parameters are manually determined by a designer of the motherboard and permanently stored in external read-only memory (ROM) (not shown). After power is turned on, the fixed initial parameters are loaded to a clock forwarding circuit (not shown) for forwarding clock signals when the master circuit is initialized. Data, which is inputted to the master circuit by the clock forwarding circuit, can be processed without any abnormal operation.

Please substitute the following for the paragraph beginning at page 5, line 1:

Further, when the clock forwarding method is used, the clock forwarding circuit and the slave circuit included in the master circuit, e.g., a memory or a chip set, must be synchronized with each other. Thus, the clock forwarding circuit and the slave circuit must be reset at the same time, which only occurs through a system reset. Also, due to noise in the slave circuit, errors can occur in data transmission.